

Fig.2 (Prior Art)

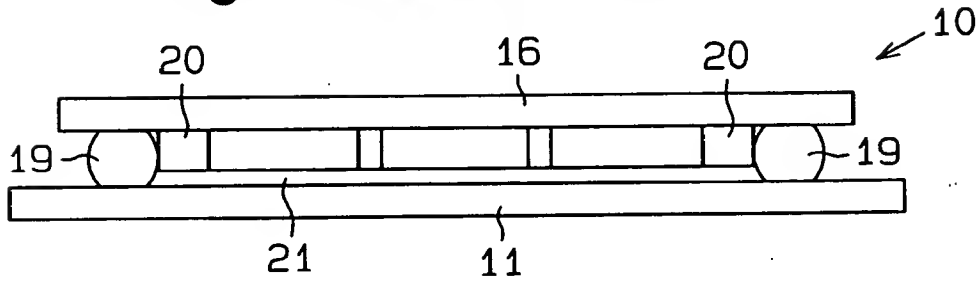


Fig.3 (Prior Art)

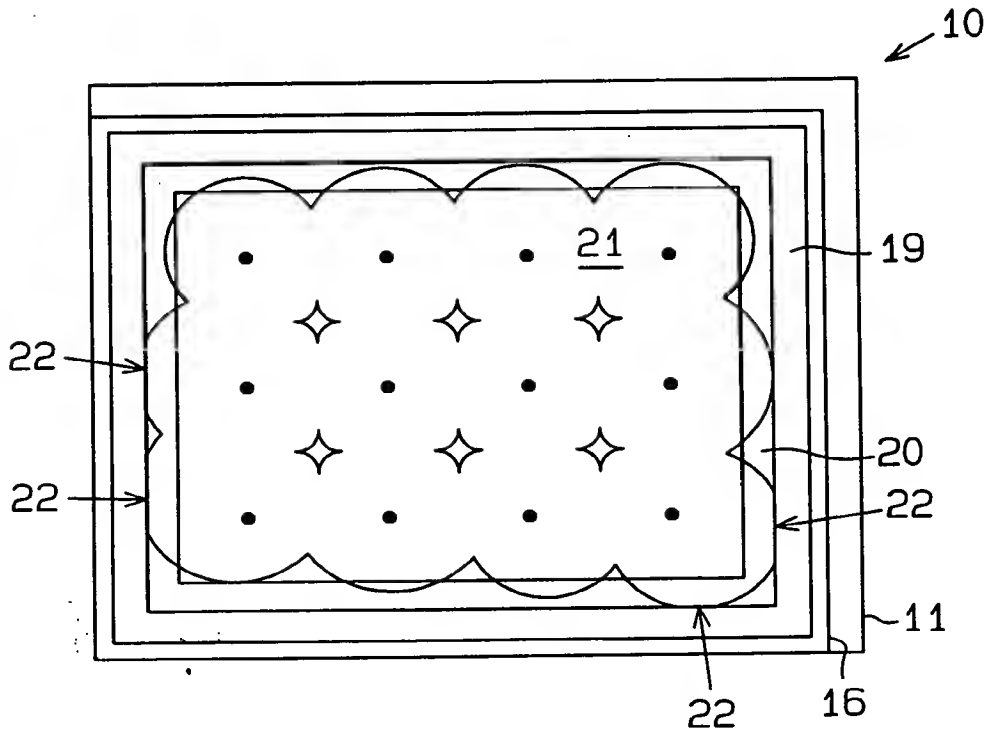


FIG. 2 (Prior Art)

Fig.4A

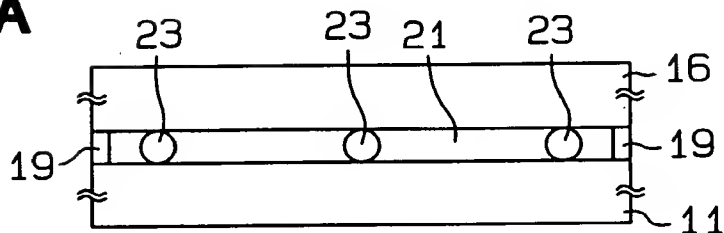


Fig.4B

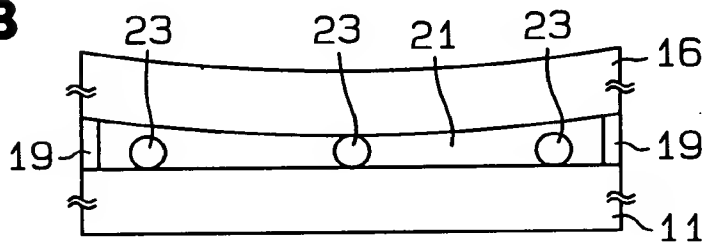


Fig.4C

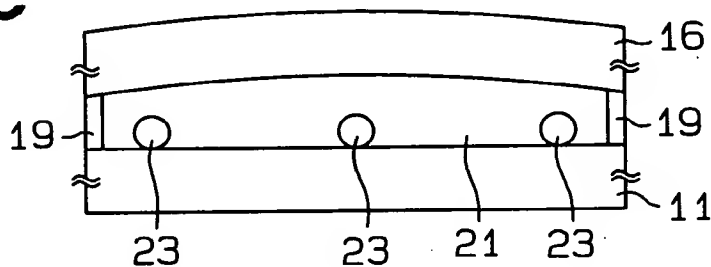


FIG. 4A, 4B, 4C

Fig. 5

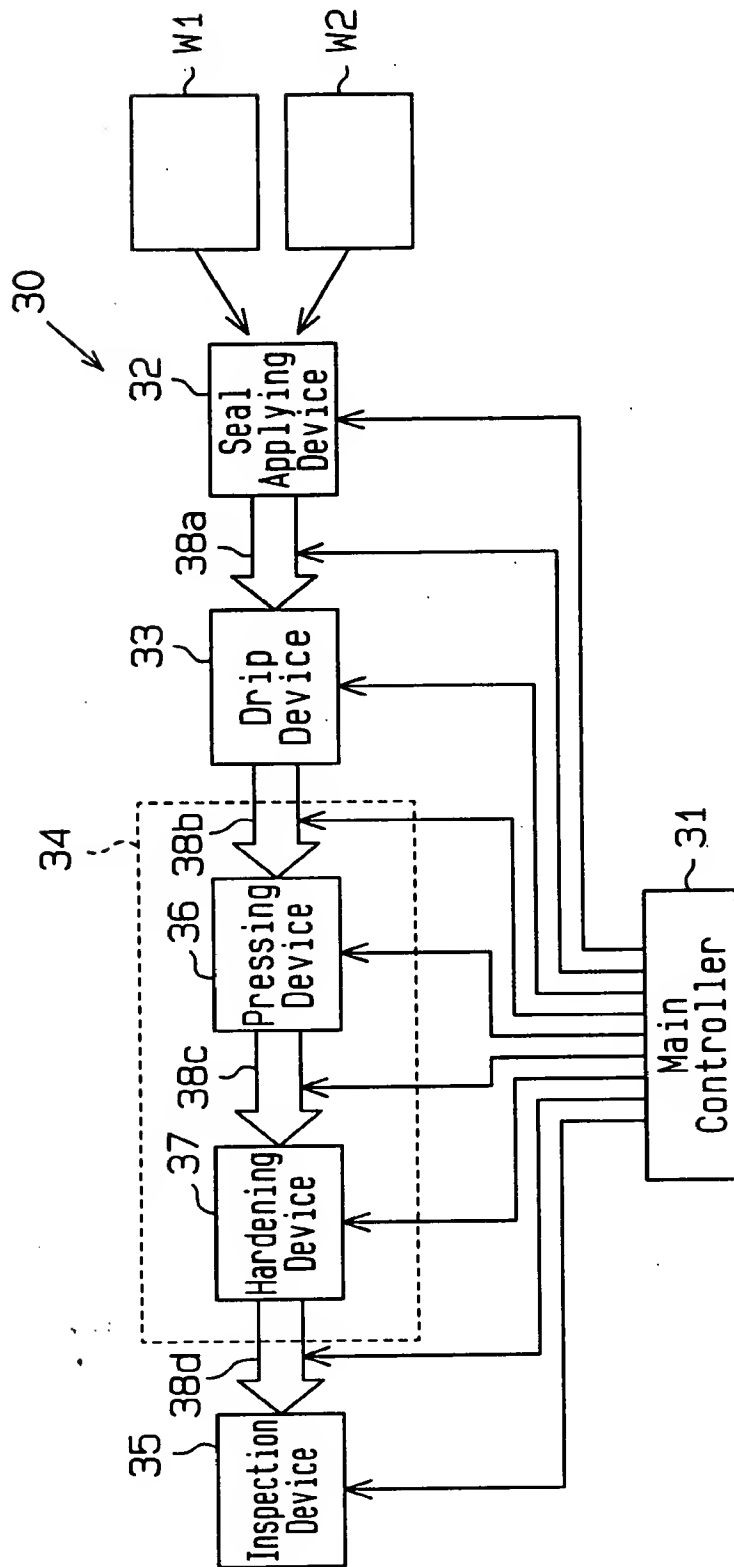


Fig. 6

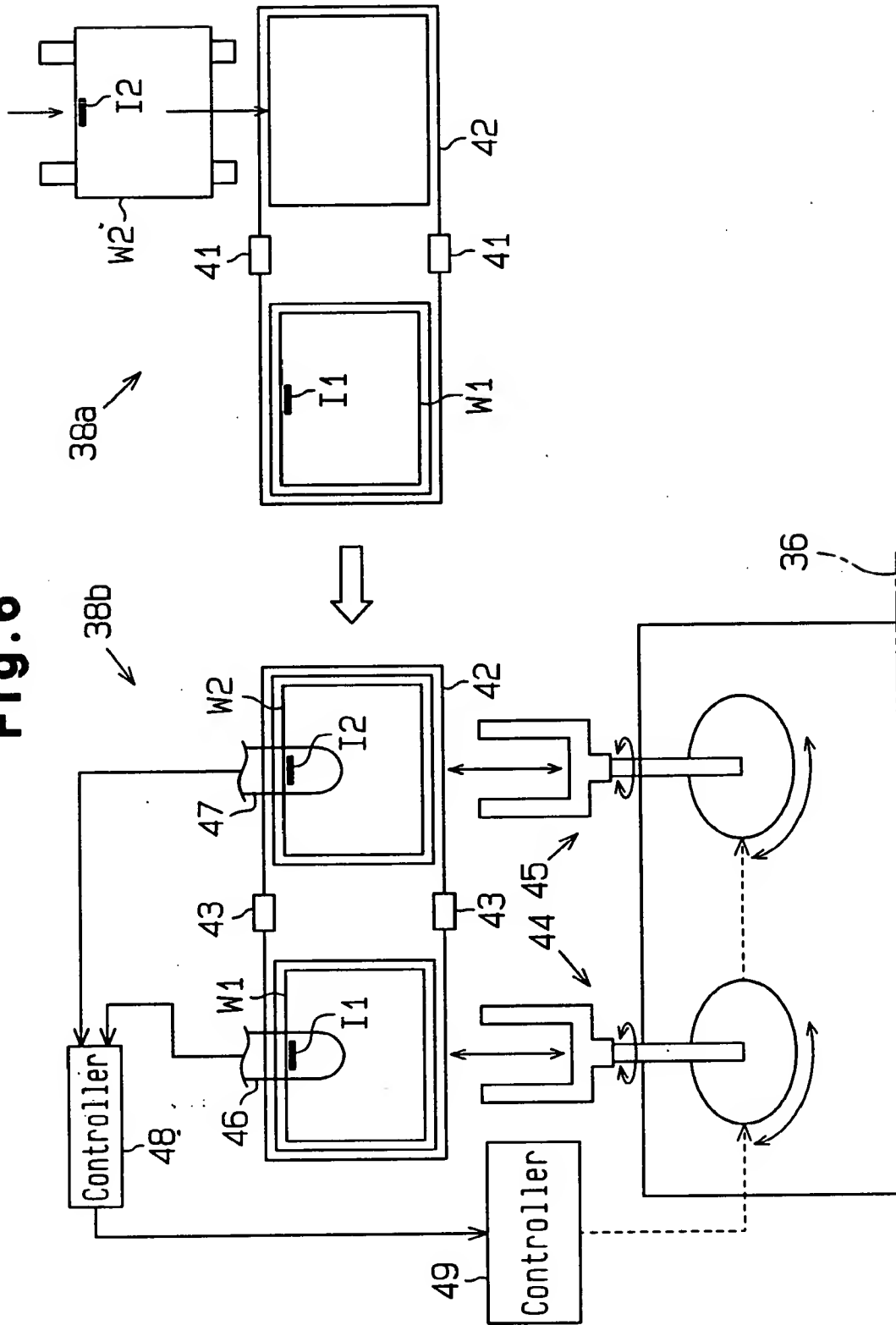


FIG. 6

Fig.7

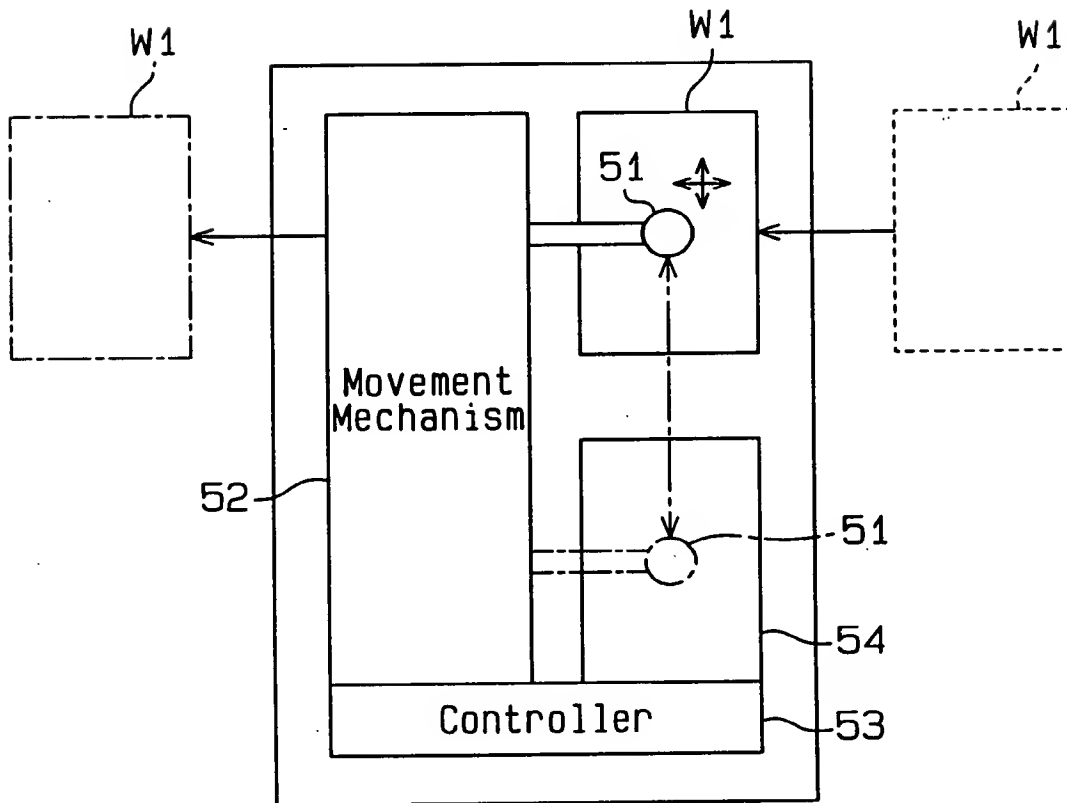


FIG. 7

Fig. 8A

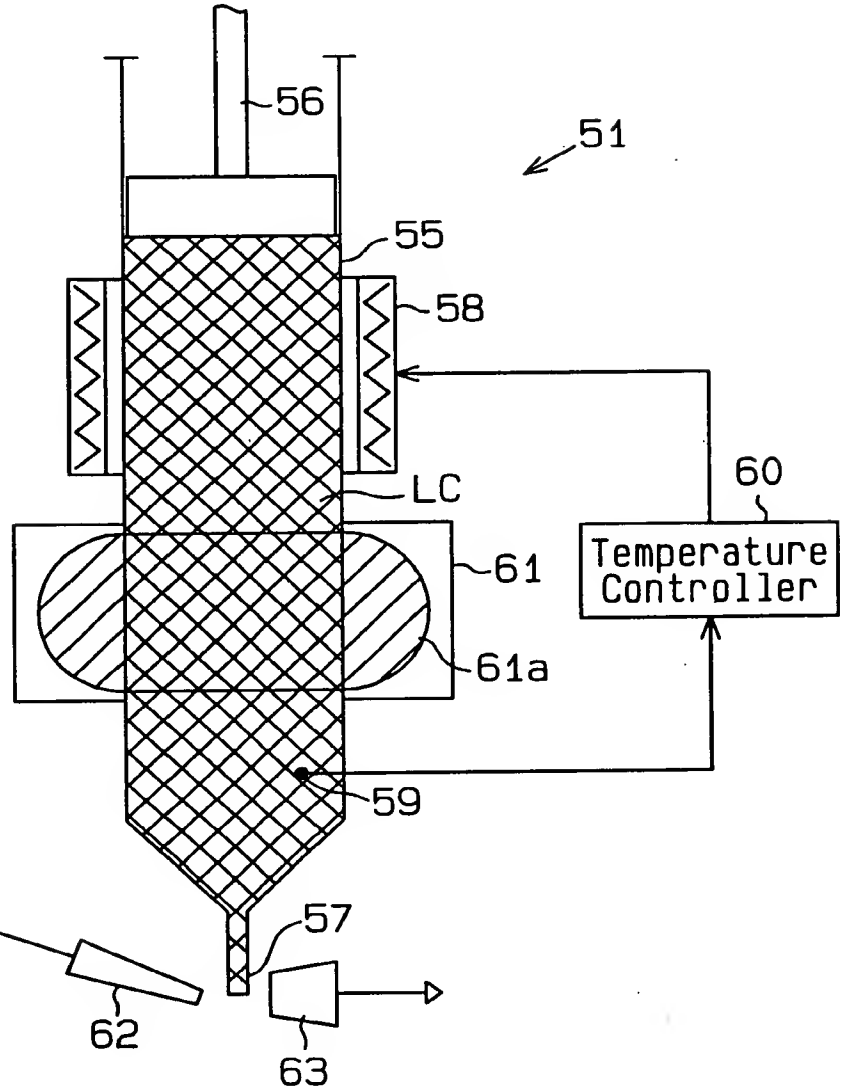


Fig. 8B

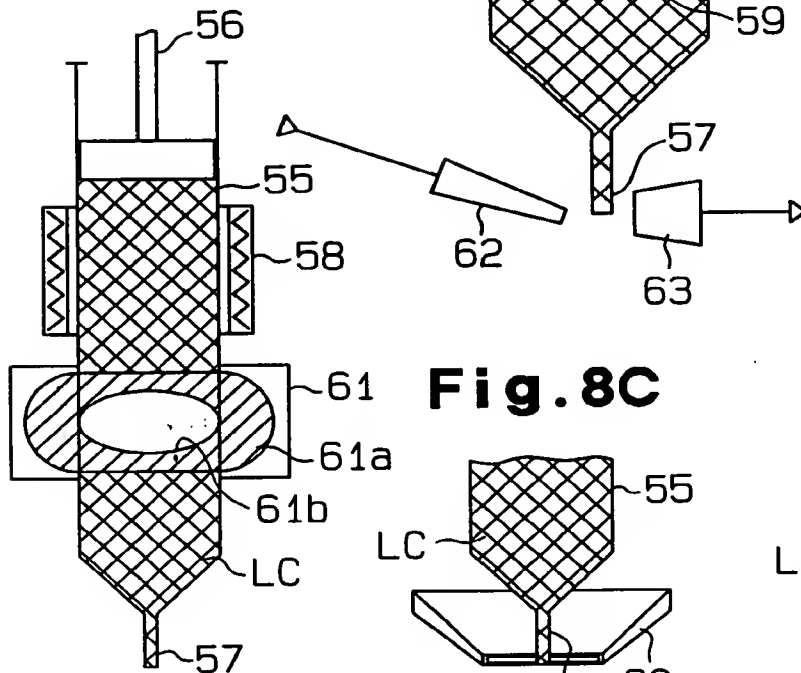


Fig. 8C

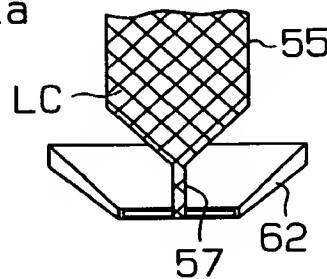


Fig. 8D

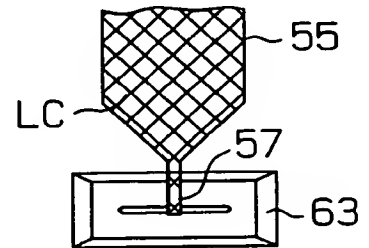


Fig. 9

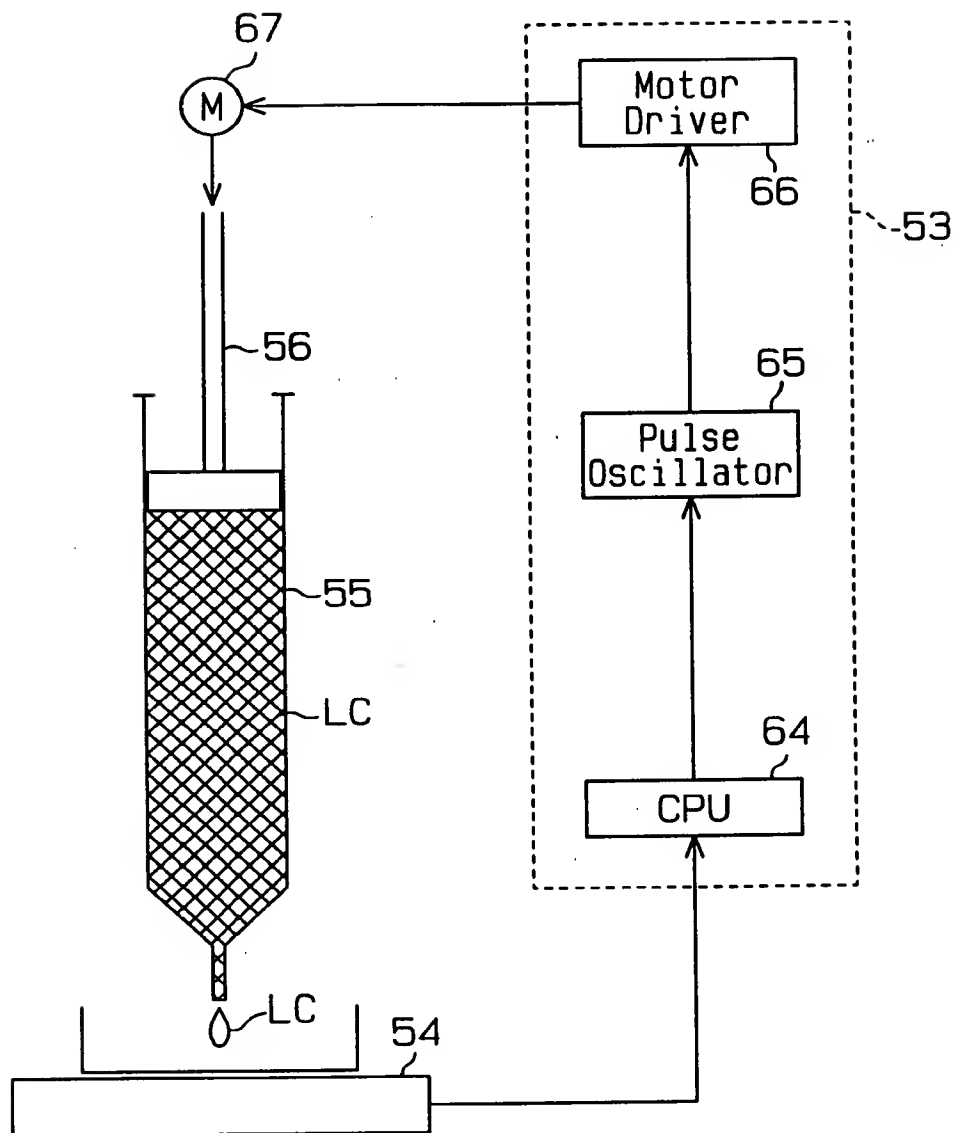


Fig.10

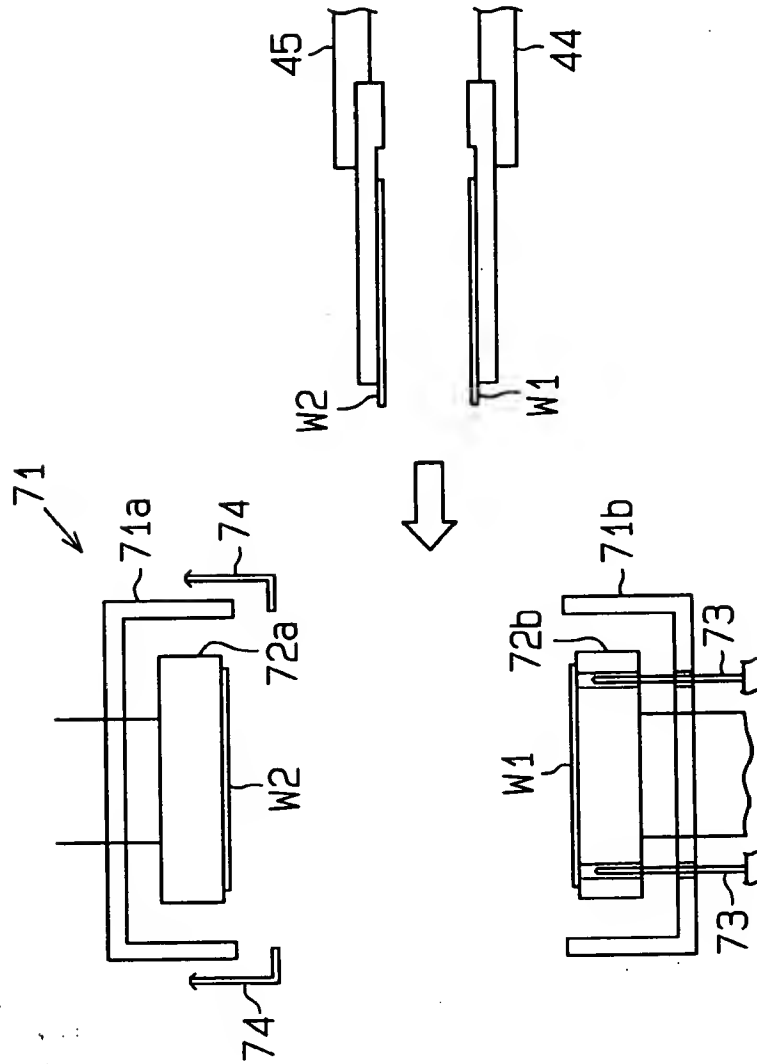


Fig.11

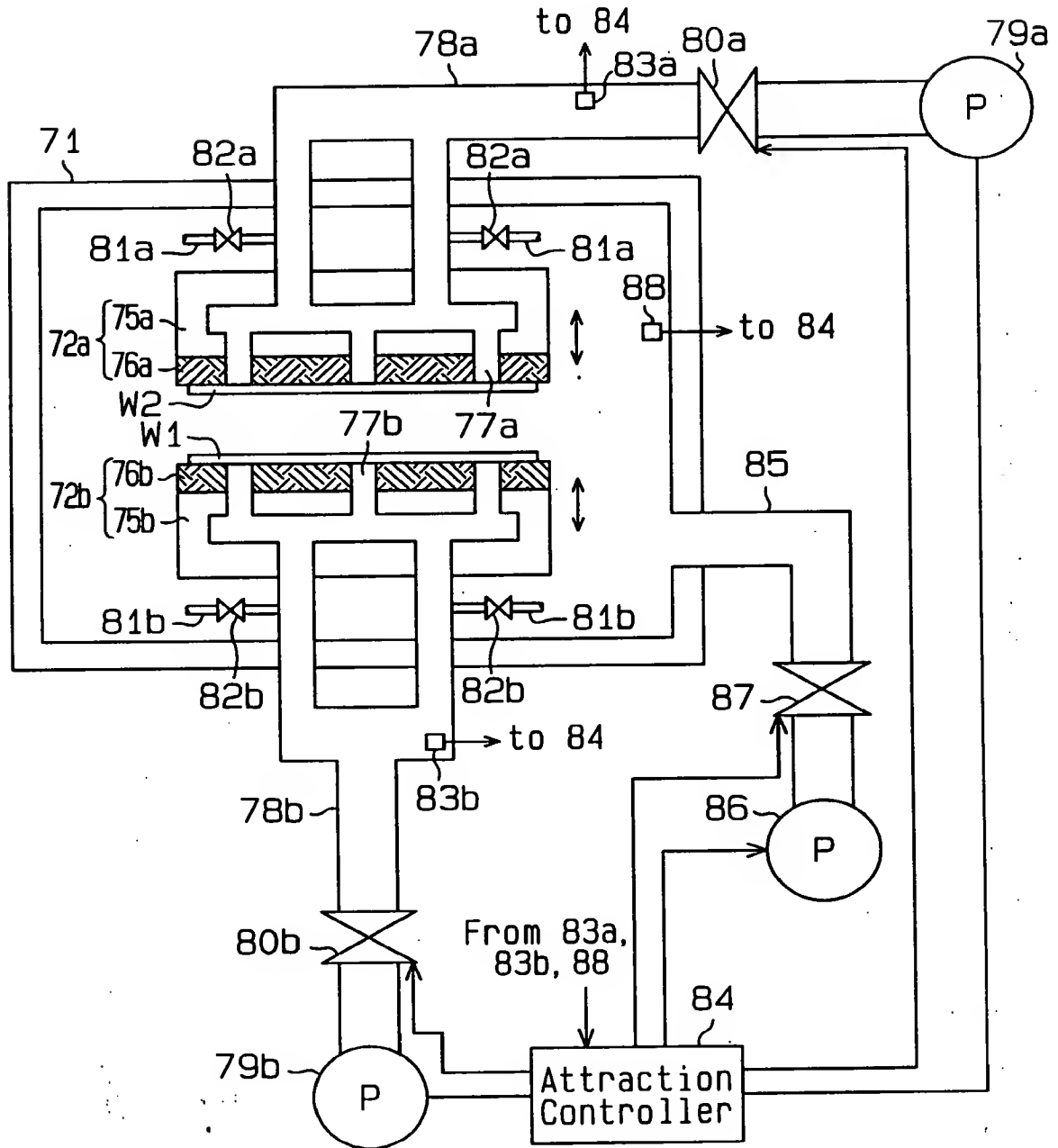


FIG. 11

Fig.12A

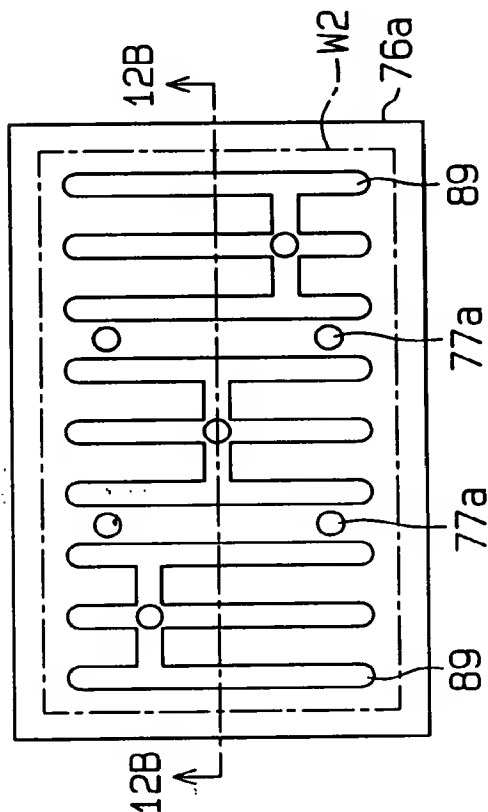


Fig.12C

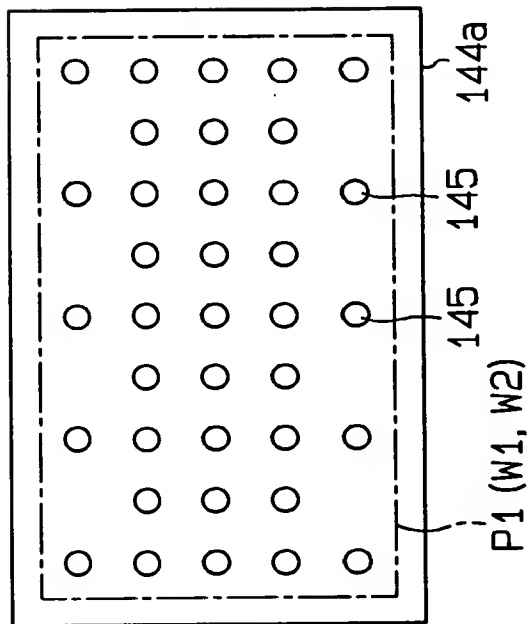


Fig.12B

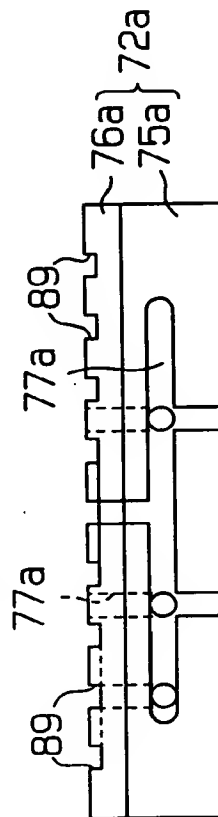


Fig.12D



This cross-sectional view shows a semiconductor device with a substrate 91a. A component forming area is defined by a width W2. The area includes a layer 94a and a layer 97. A gate structure 98 is formed on the surface, with a gate electrode 99 and a gate insulating layer 100. A contact point 100a is shown on the surface of the component forming area. A connection line is connected to the gate electrode 99 and the contact point 100a. A label FG is connected to the substrate 91a.

Fig.14A

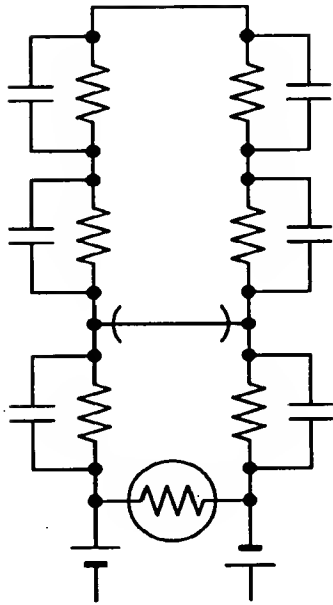


Fig.14B

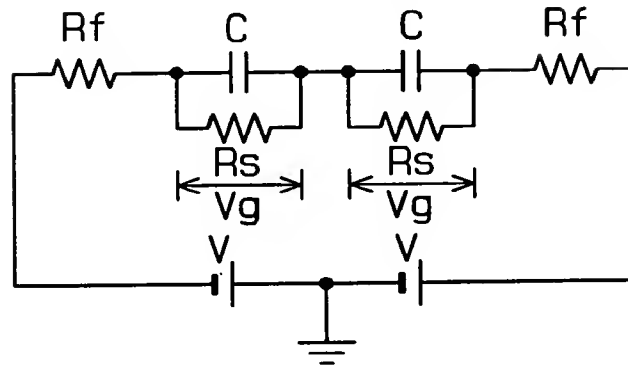


Fig.15

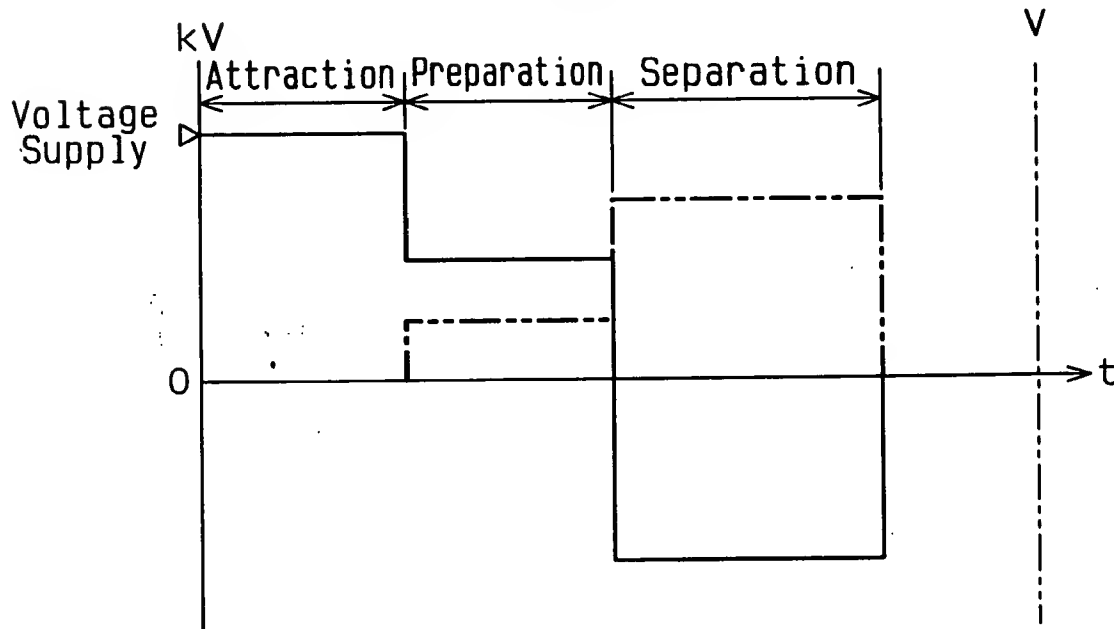


Fig.16A

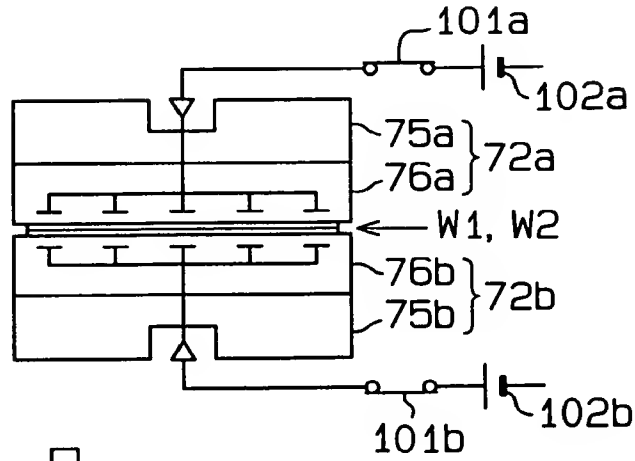


Fig.16B

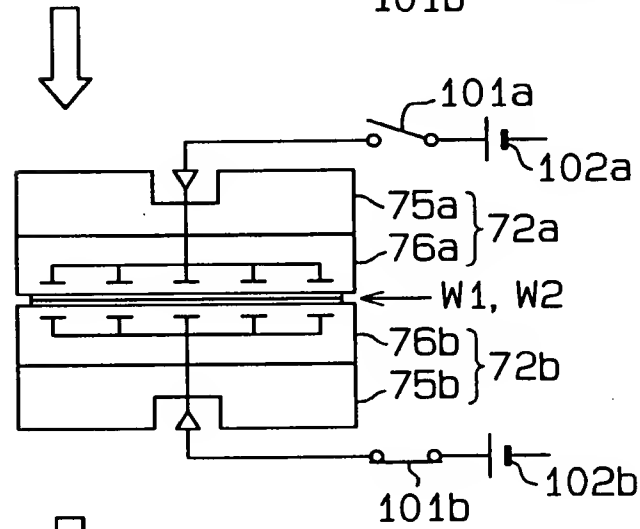


Fig.16C

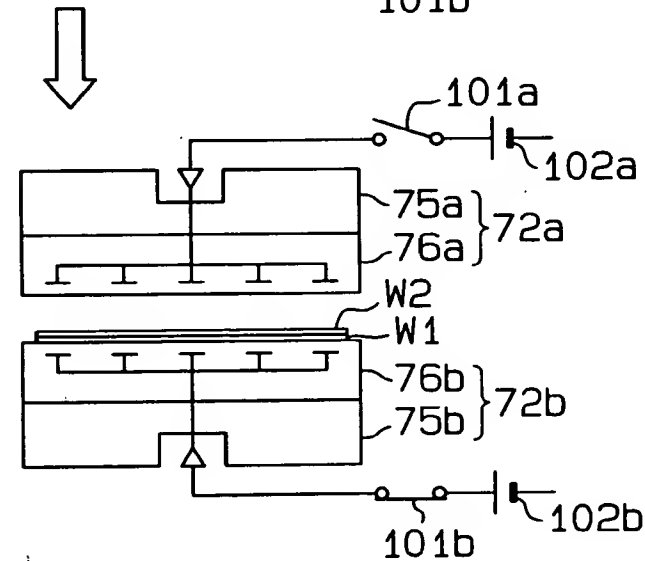


FIG. 16A, 16B, 16C

Fig.17

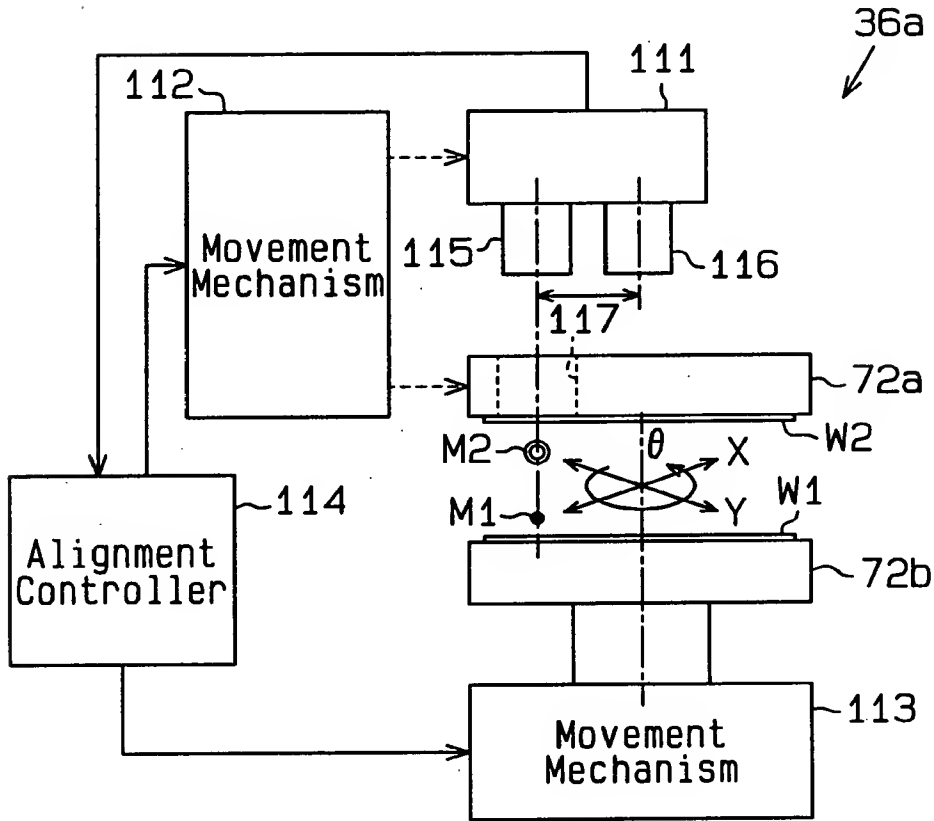
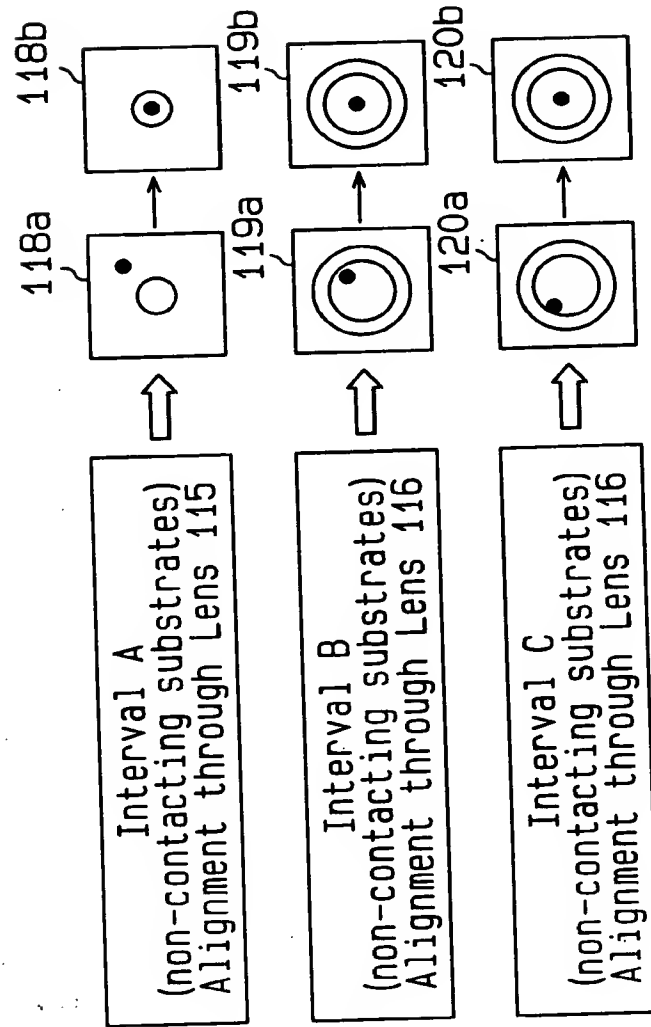


FIG. 17

Fig.18



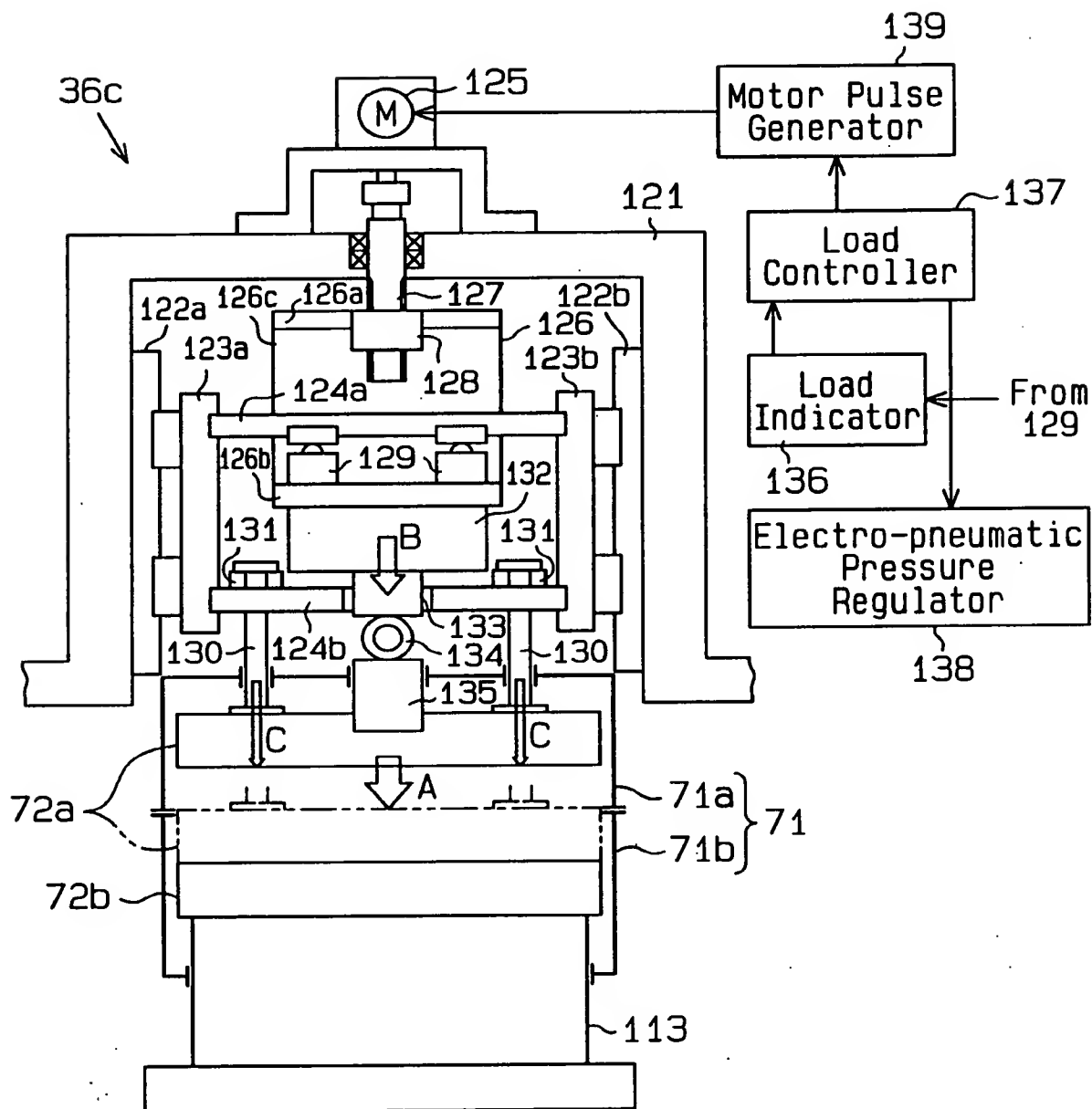
[illegible]

Fig.20

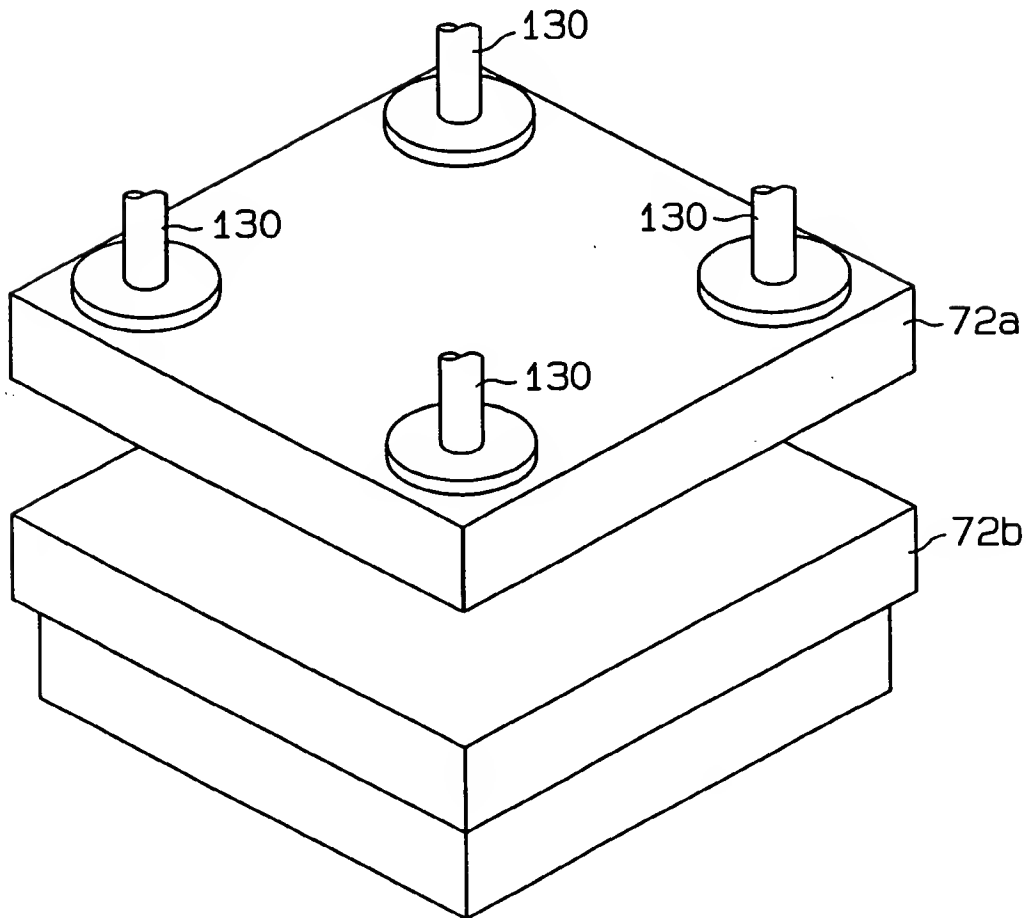


FIG. 20

Fig. 21

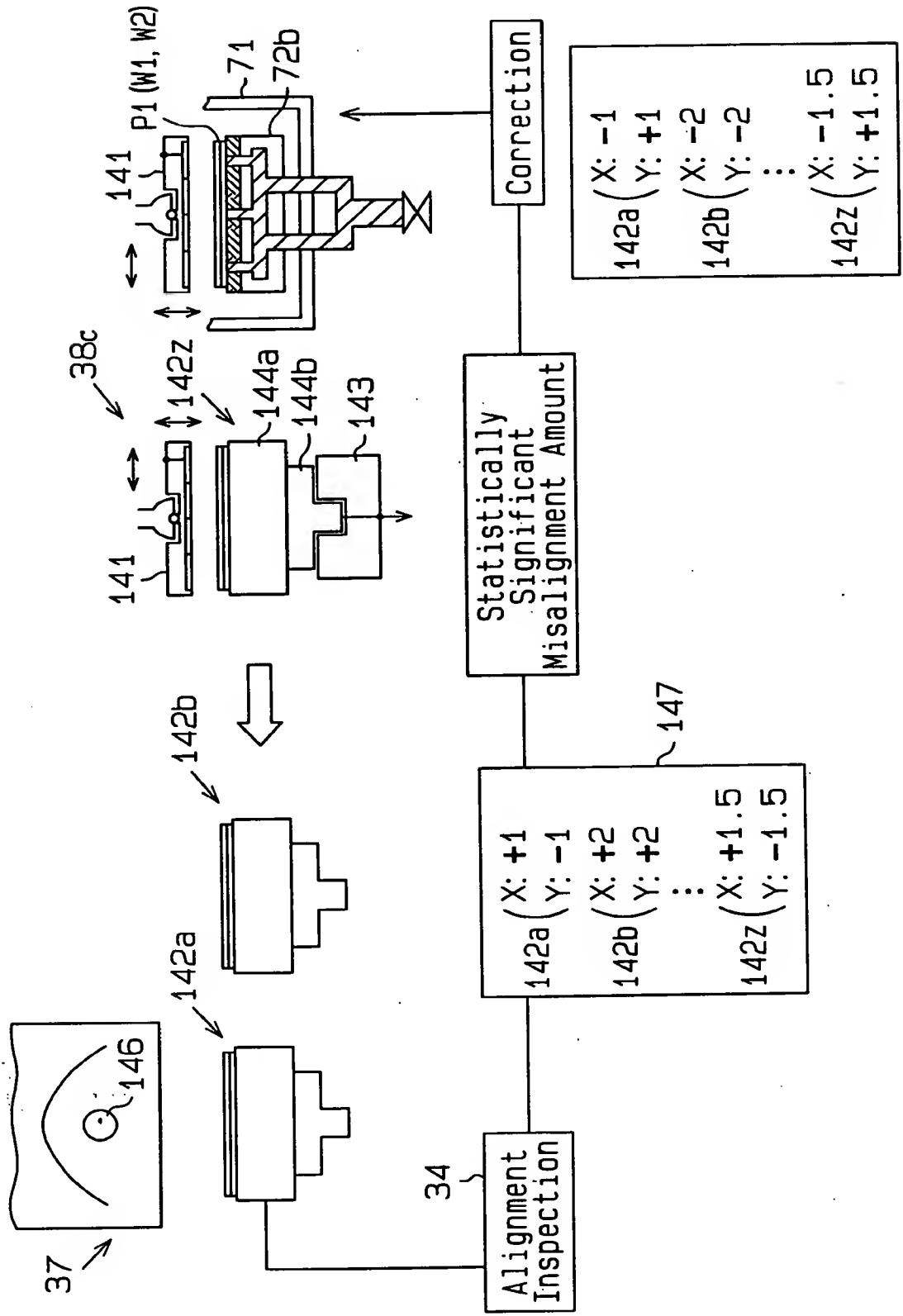


Fig. 22

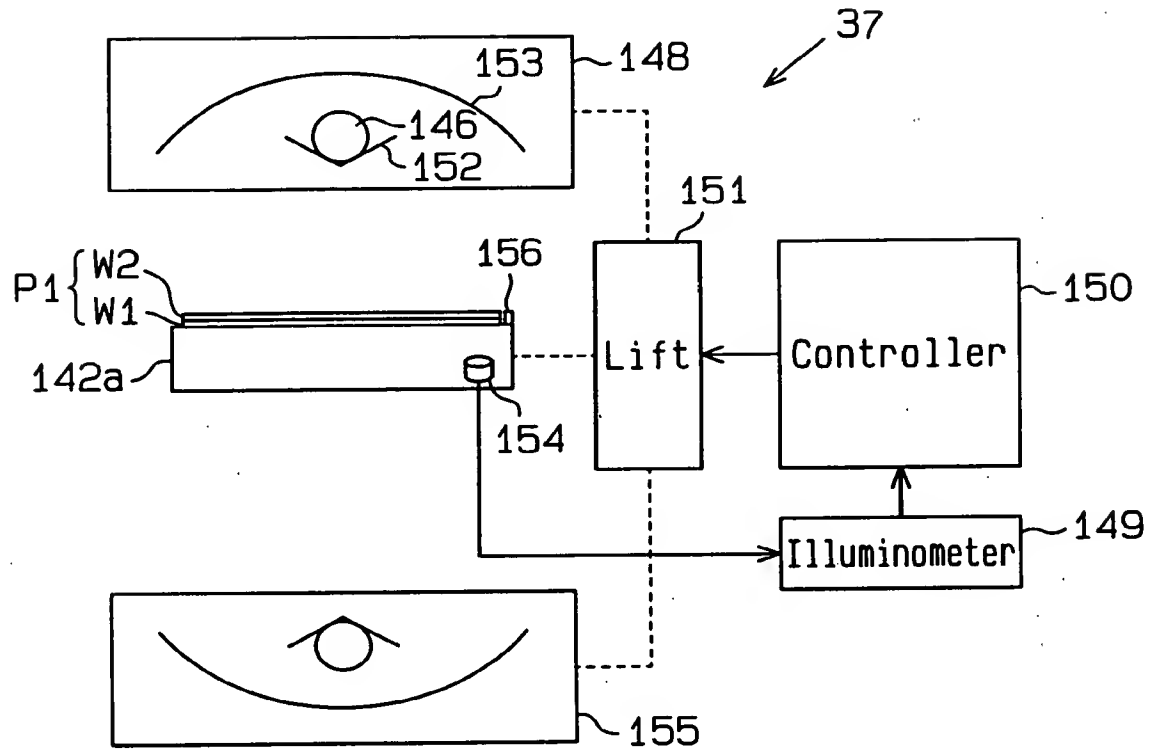


Fig. 23A

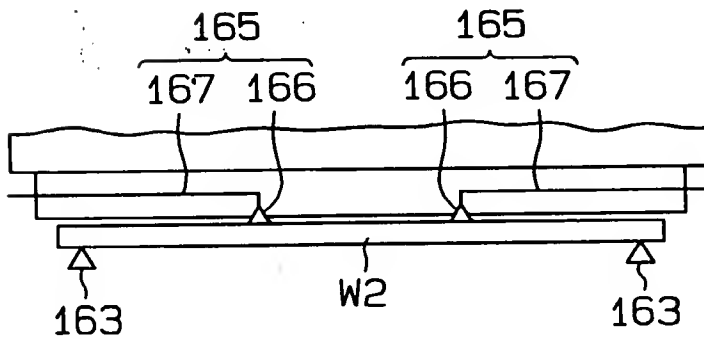


Fig. 23B

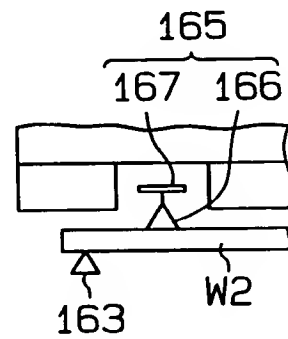


FIG. 22

Fig.24A

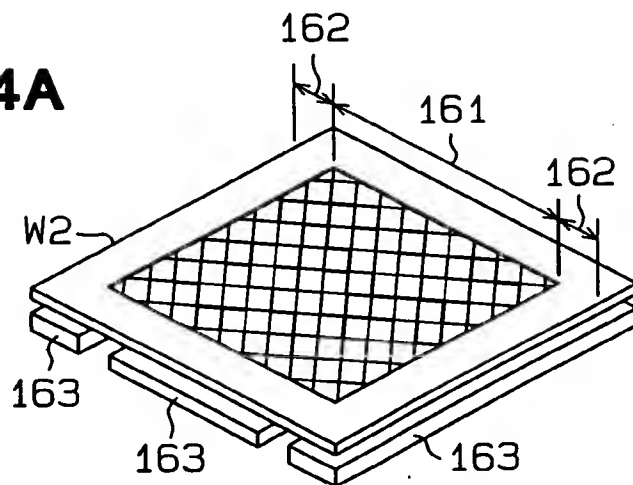


Fig.24B



Fig.25

A

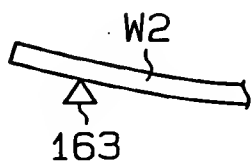


Fig.25

B

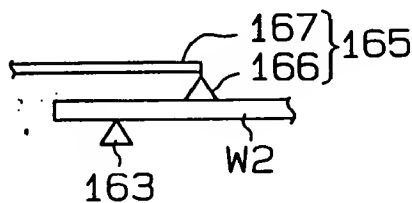


Fig.25

C

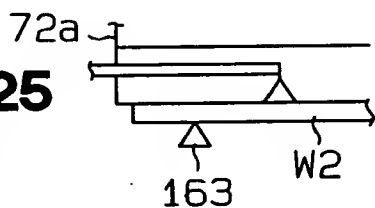


Fig.25

D

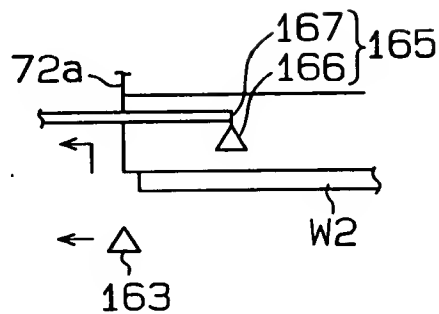


Fig.25

E

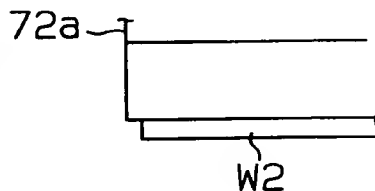


Fig. 26

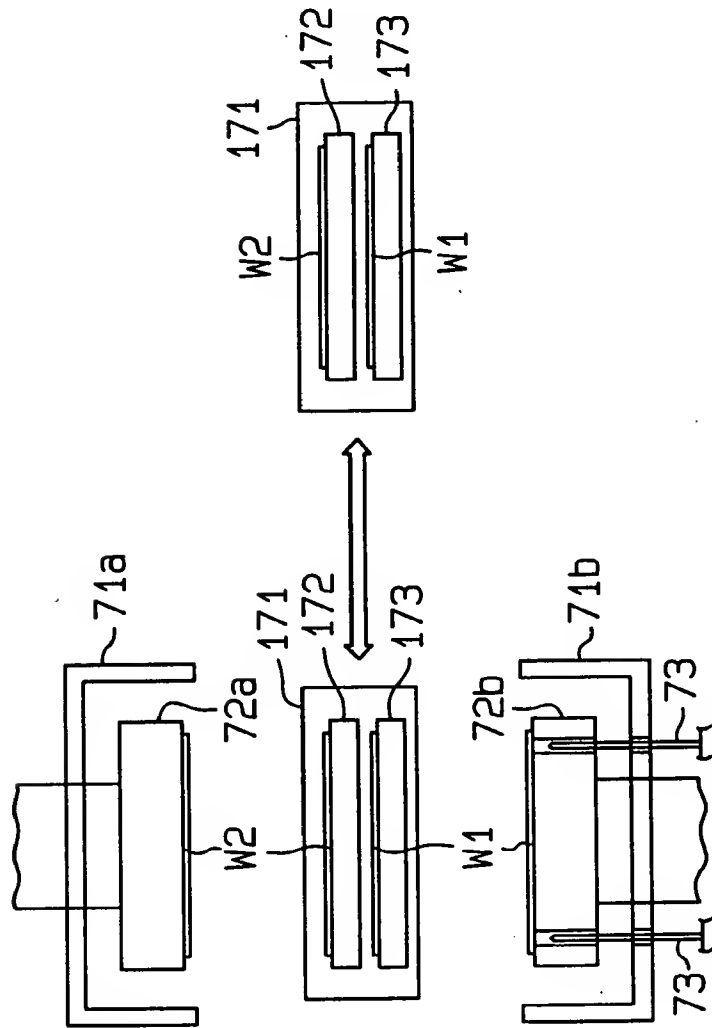


FIG. 26

Fig.27

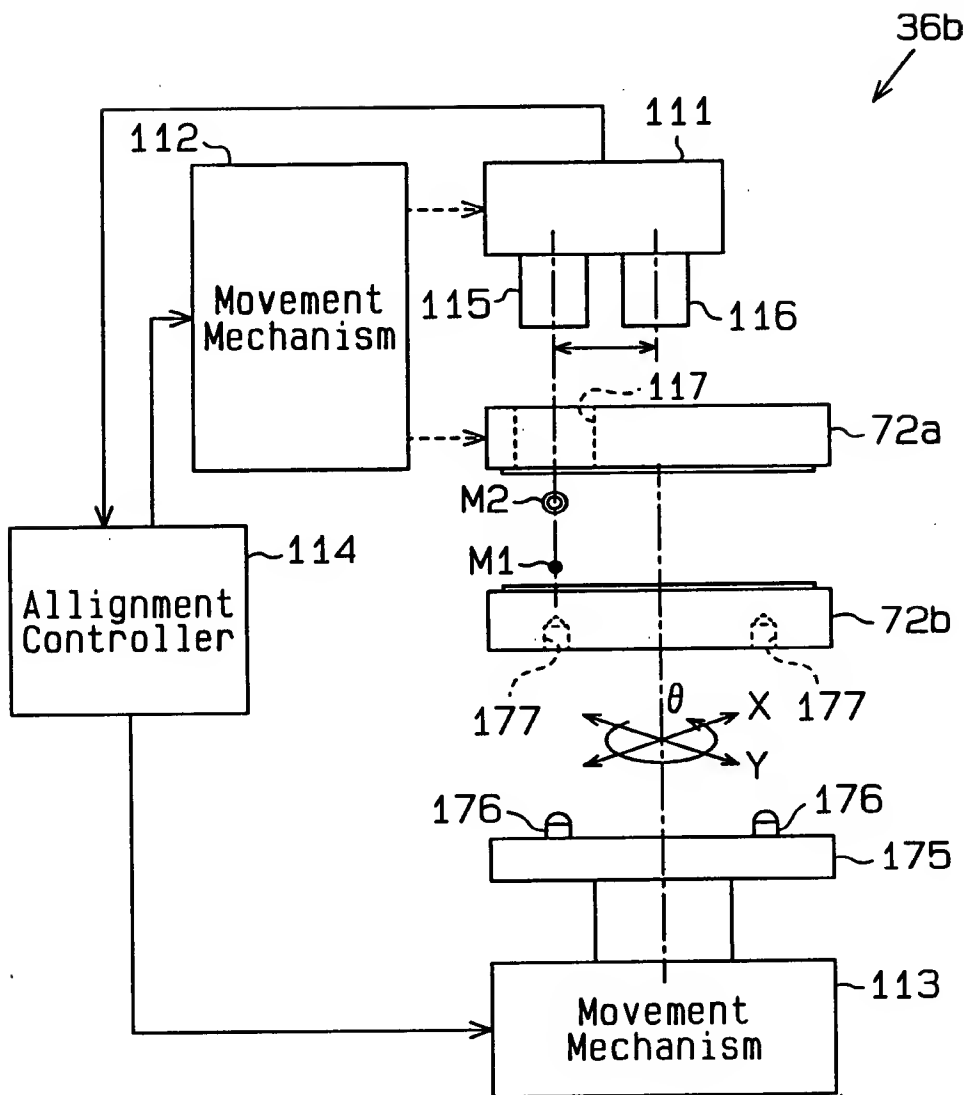


Fig. 28C

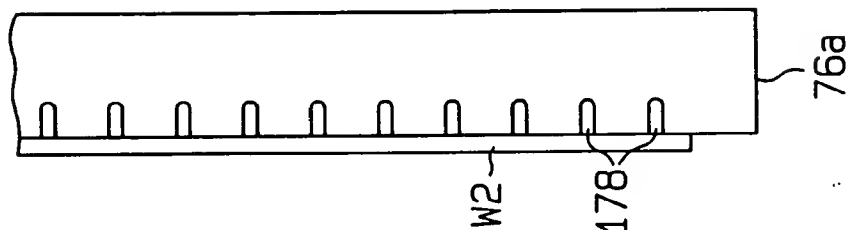


Fig. 28A

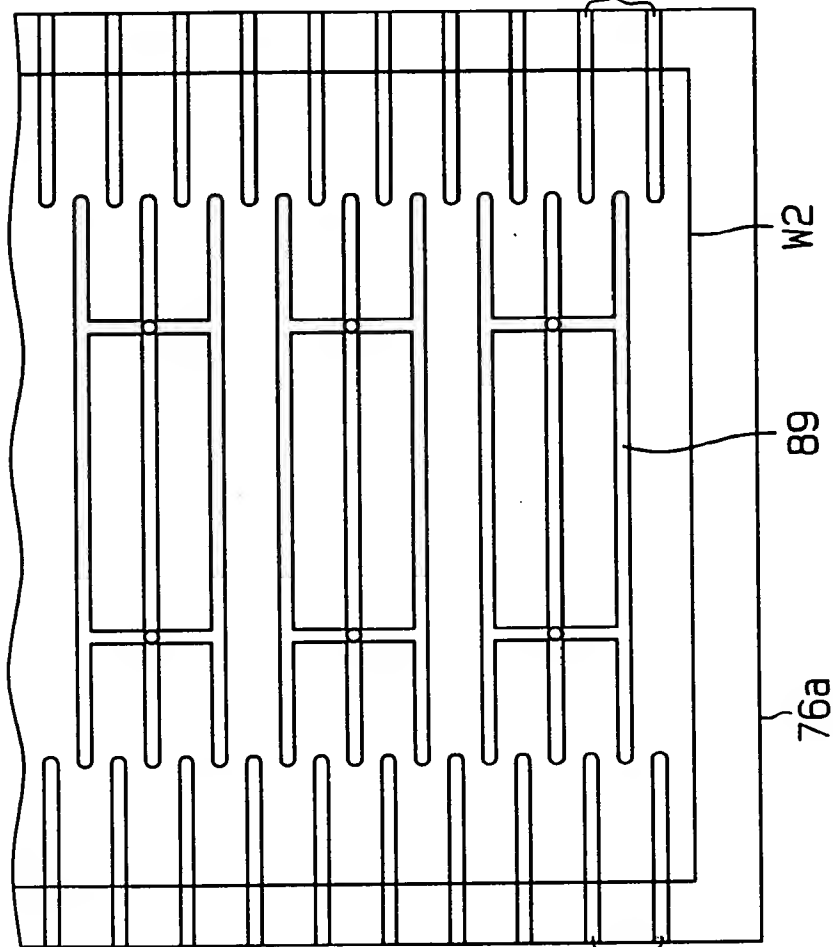


Fig. 28B

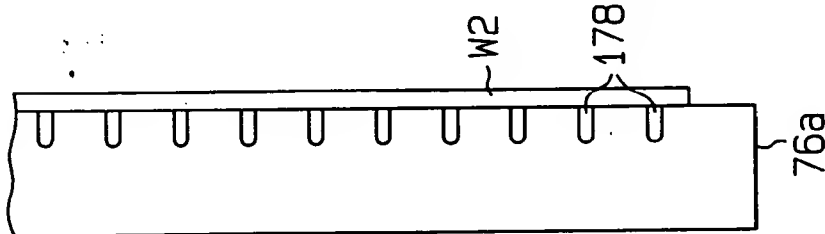


FIG. 28A

Fig. 29

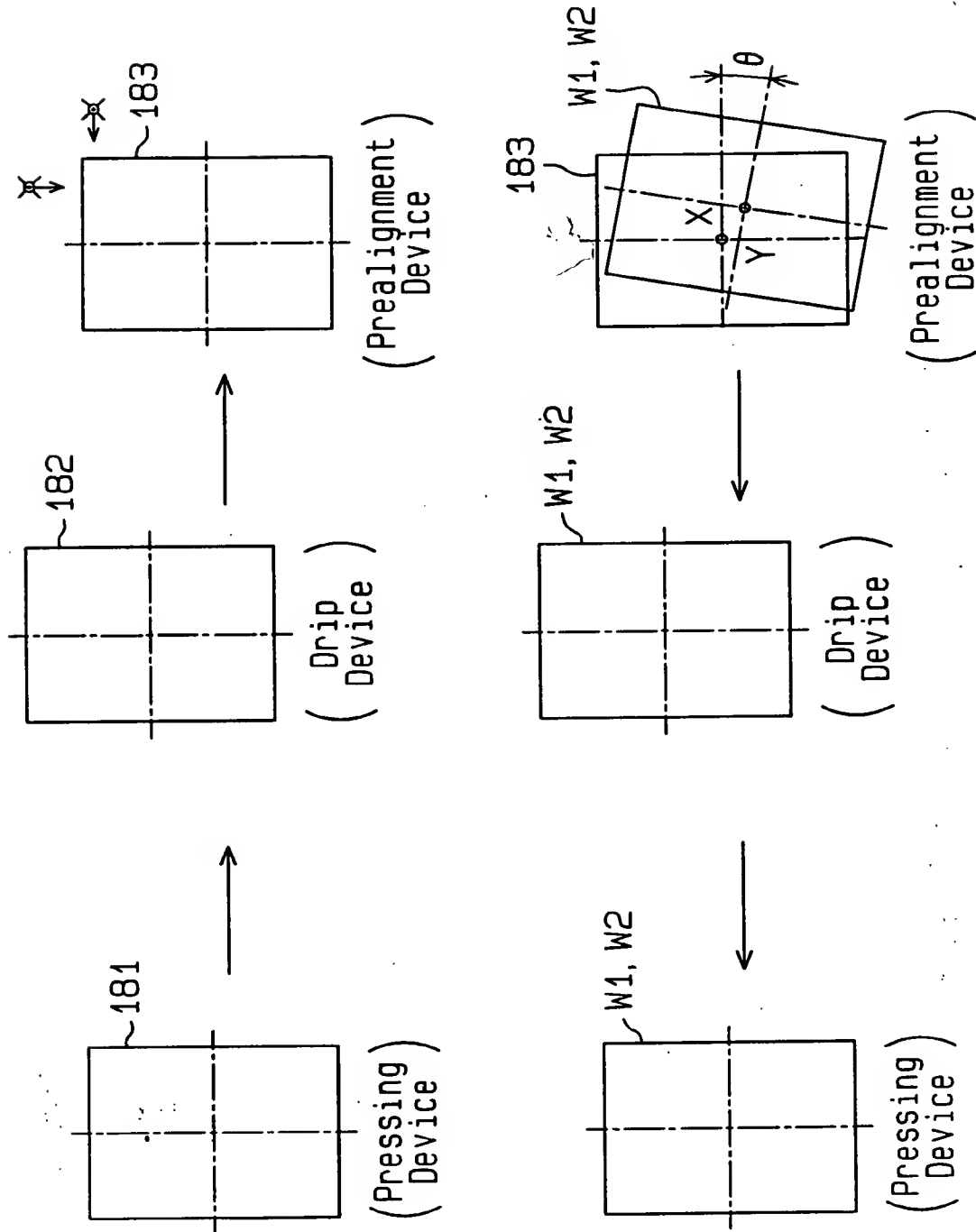


Fig.30

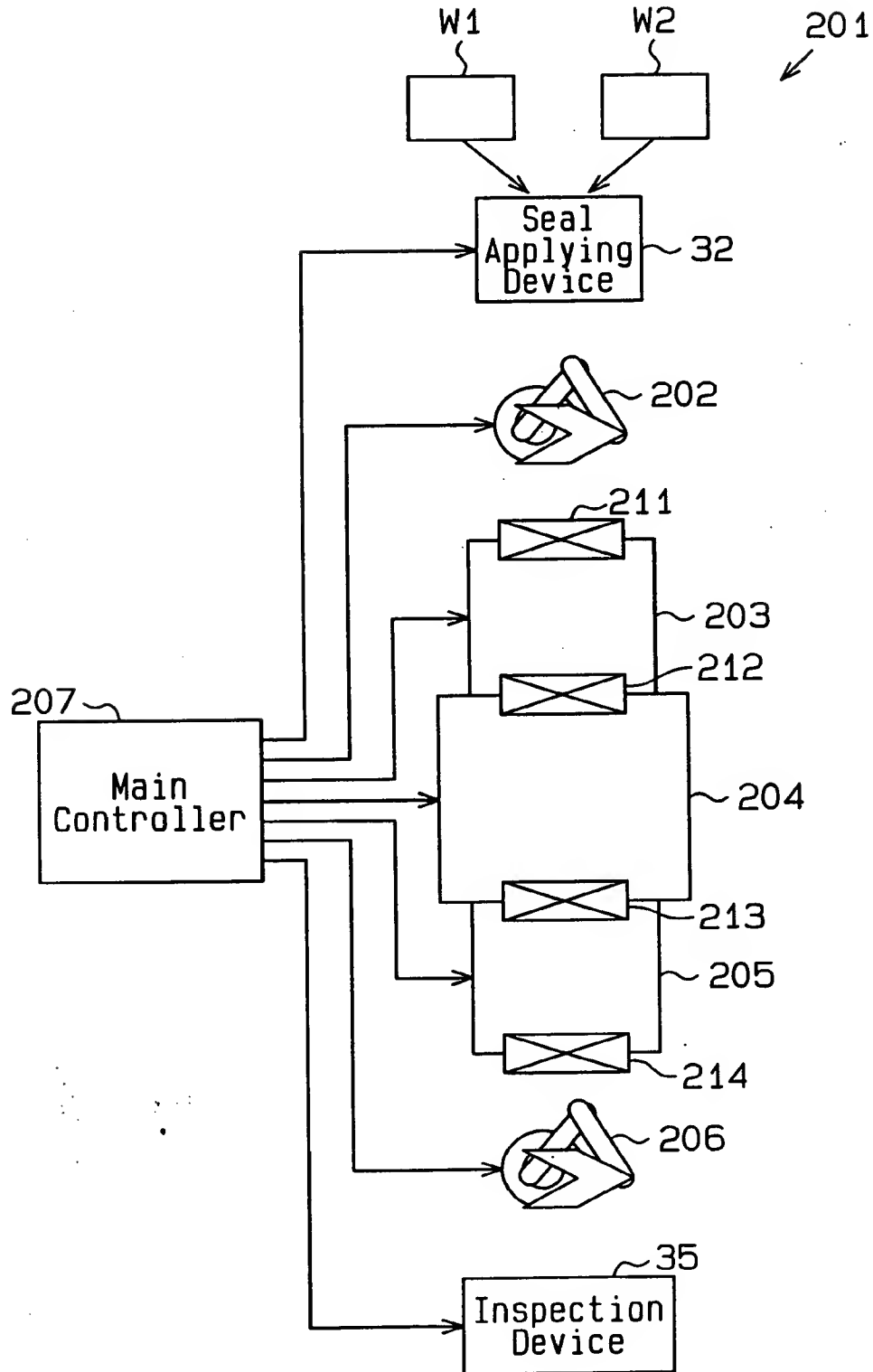


Fig. 31 B

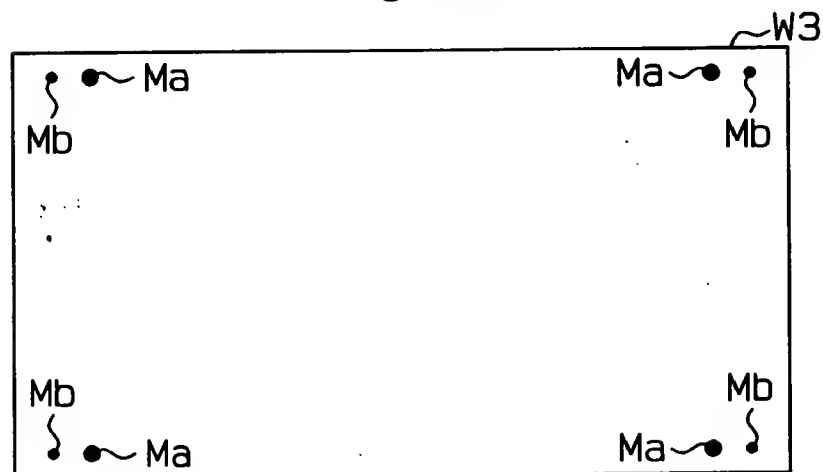


Fig. 33A

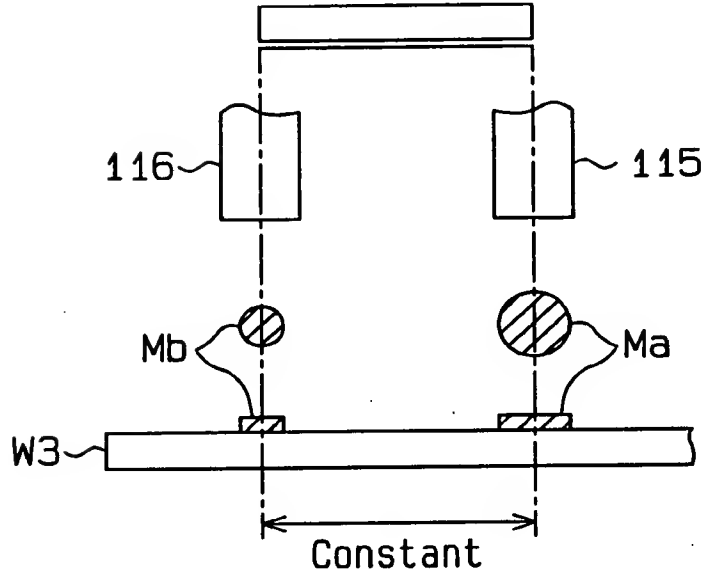


Fig. 33B

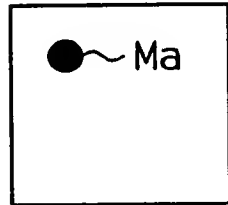


Fig. 33D

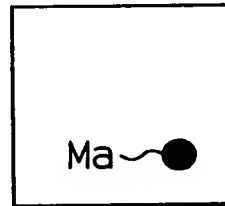


Fig. 33C

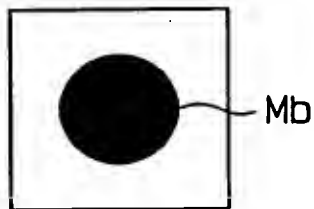


Fig. 33E

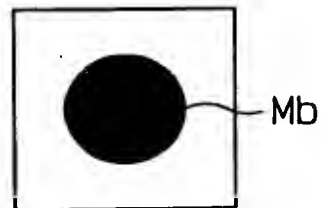


Fig. 34

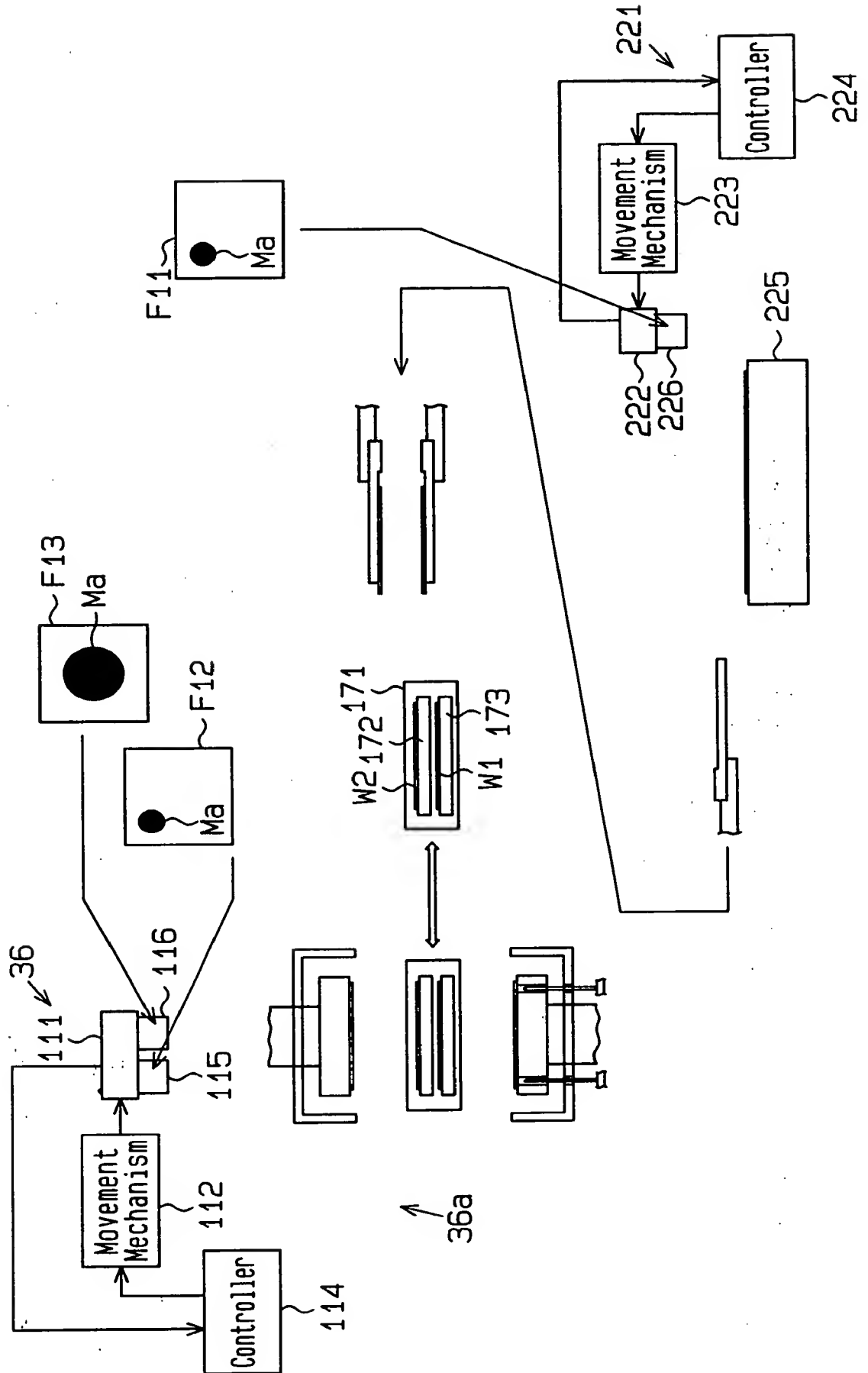


Fig.35

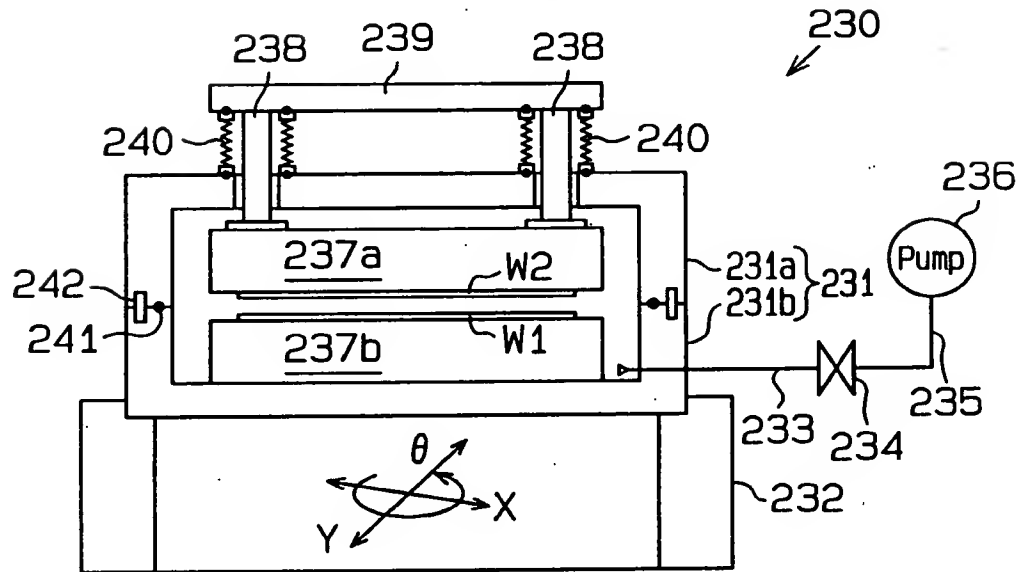


Fig.36 (Prior Art)

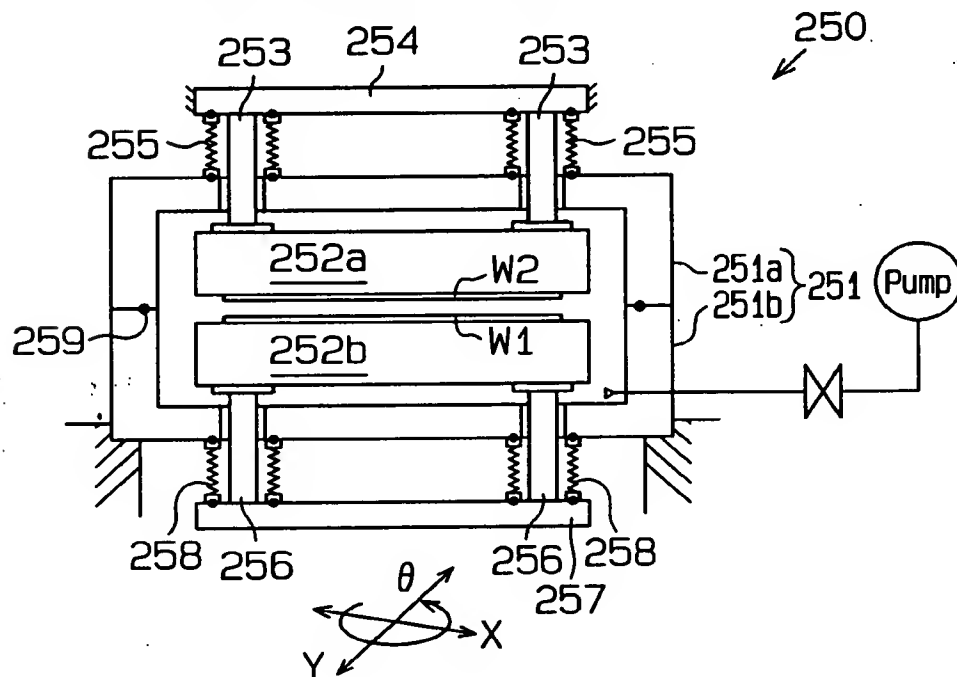


Fig.37 (Prior Art)

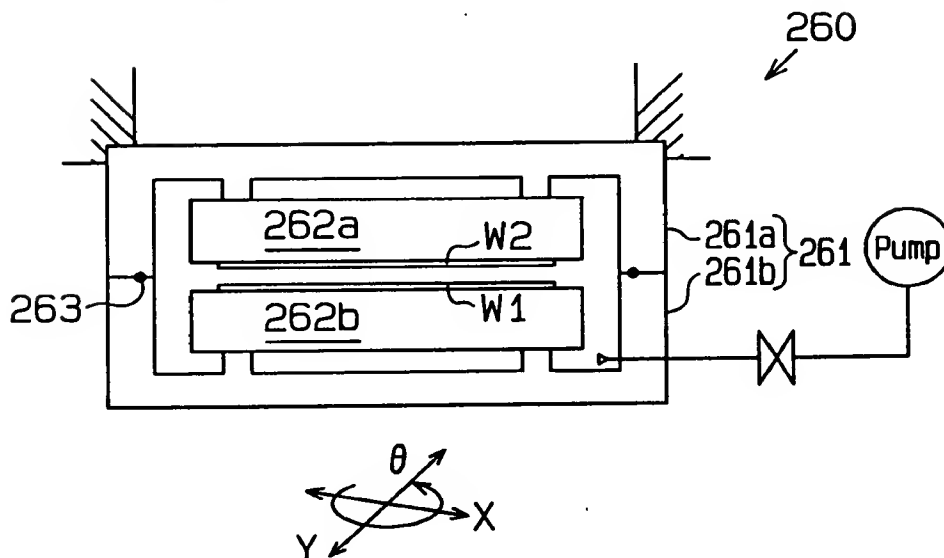


Fig.38

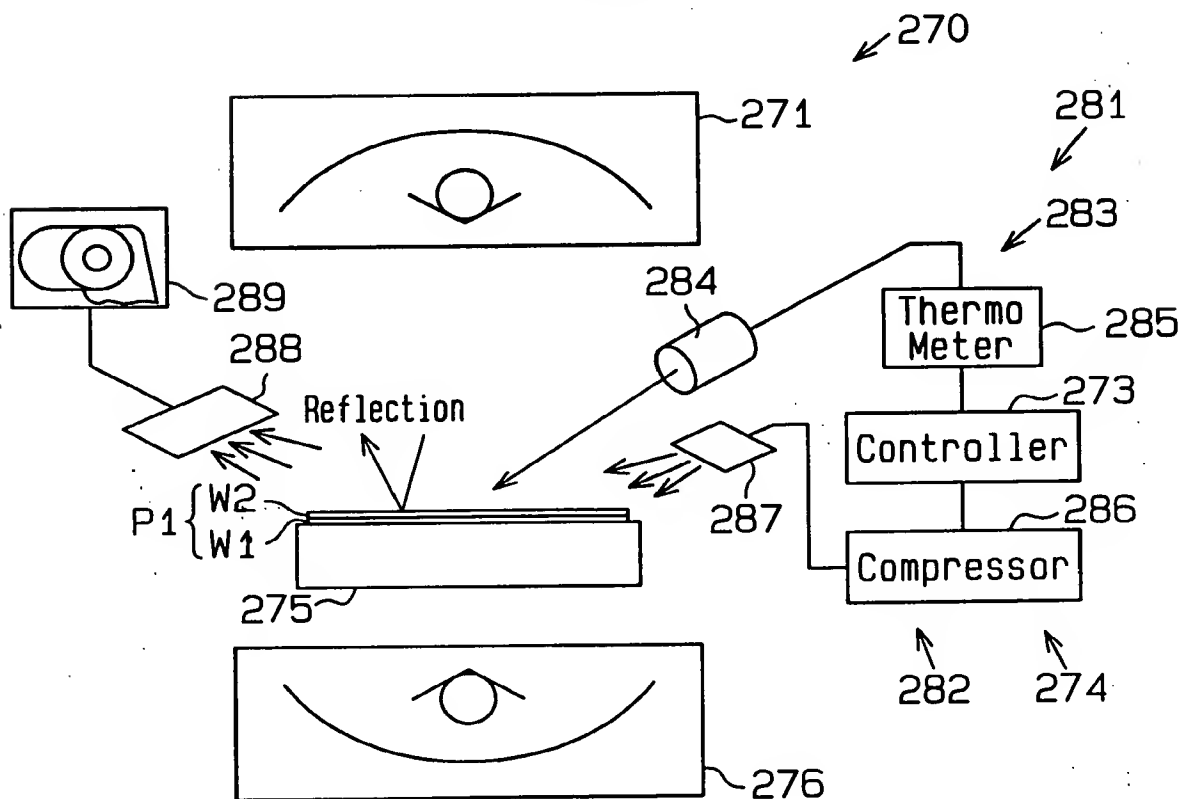


FIG. 37 (Prior Art)